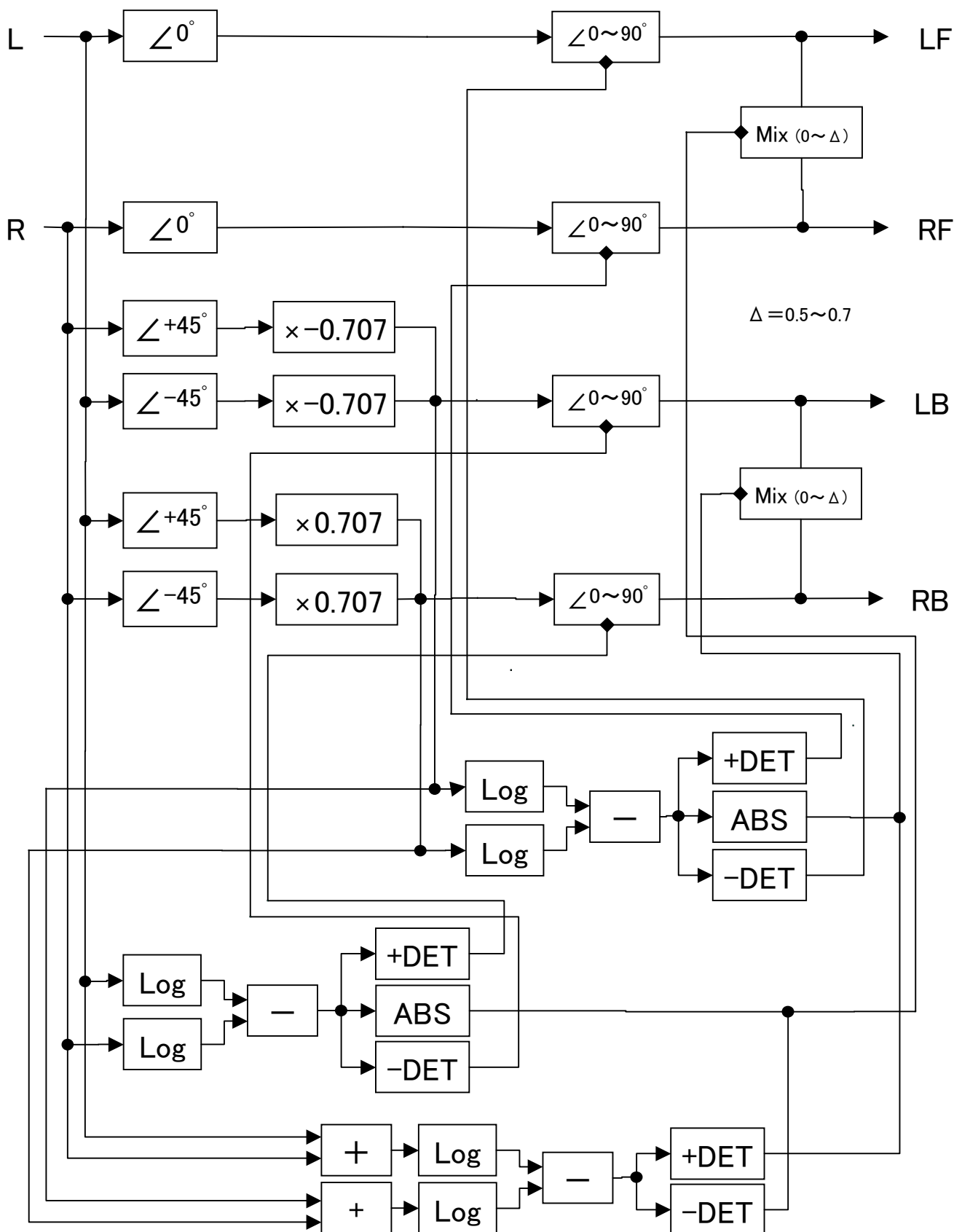




SQ Decoder with Full Logic

Fig 8