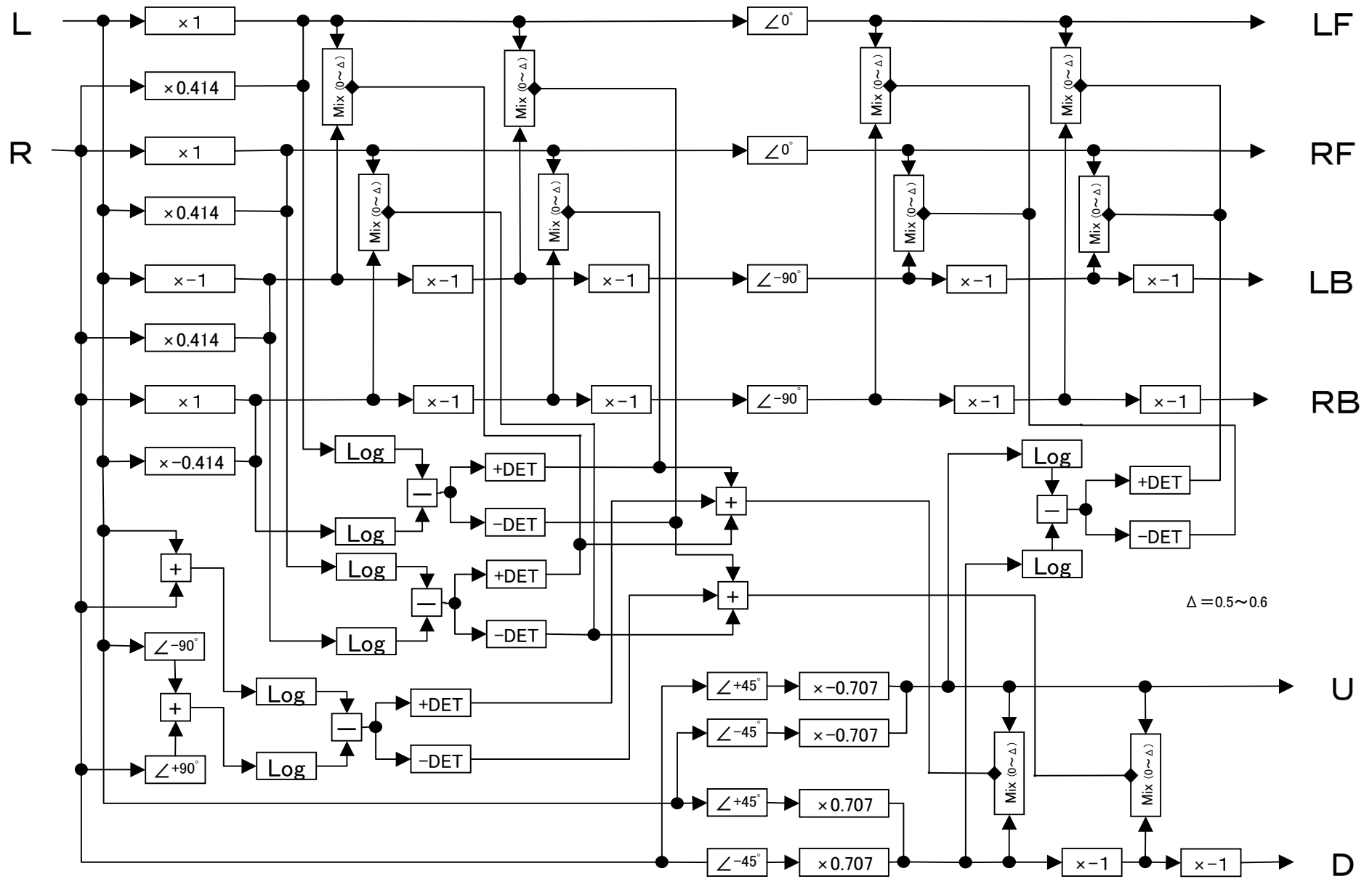




6 Channel Decoder with Logic

Fig 12