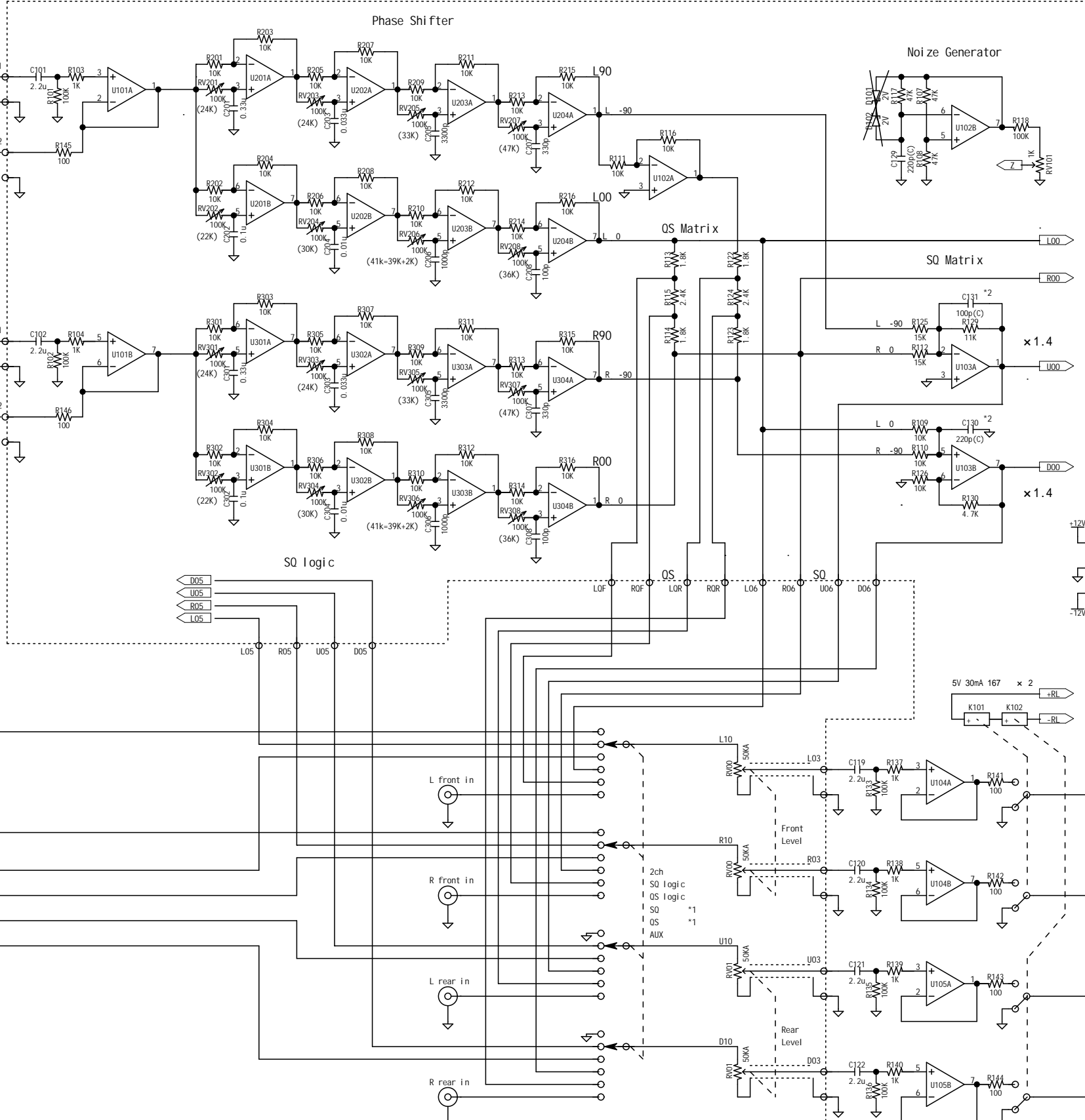
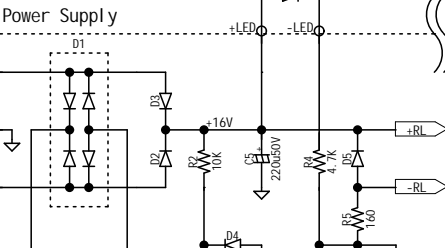
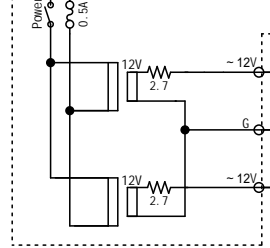
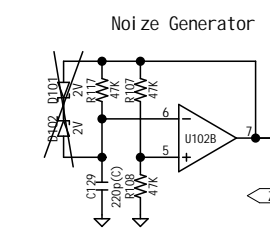


PCB 1

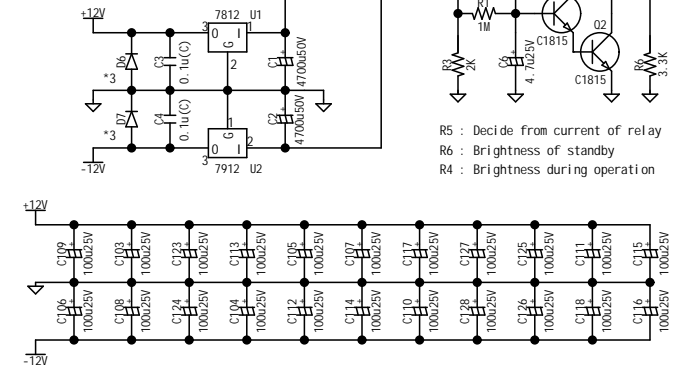


to PCB 2  
 S0GD  
 Lout  
 Rout  
 OSLFOUT  
 OSRFOUT  
 OSLBOUT  
 OSRBOUT

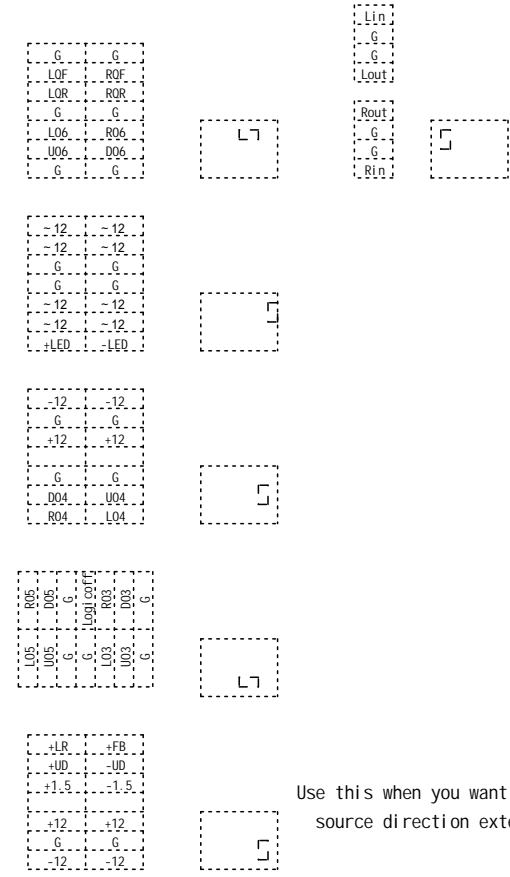
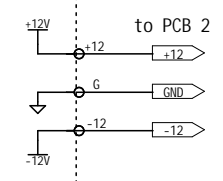
\*1 There is almost no opportunity to listen without logic, so there is no problem even without this mode.



R5 : Decide from current of relay  
 R6 : Brightness of standby  
 R4 : Brightness during operation



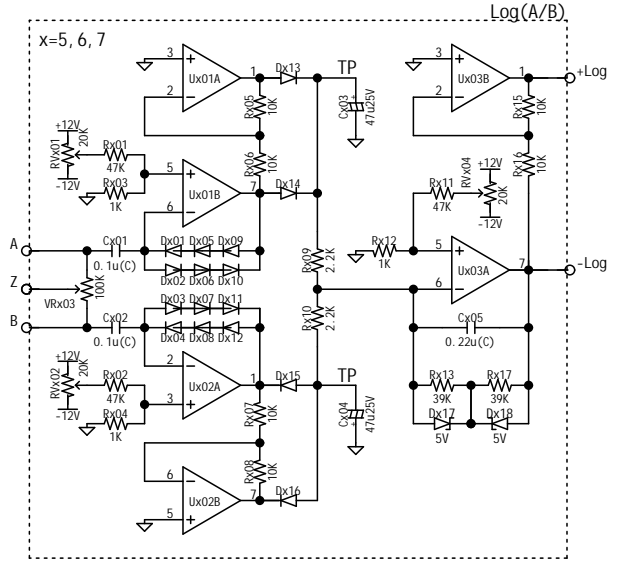
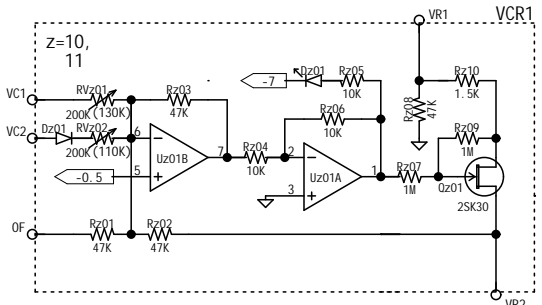
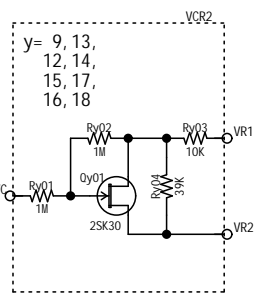
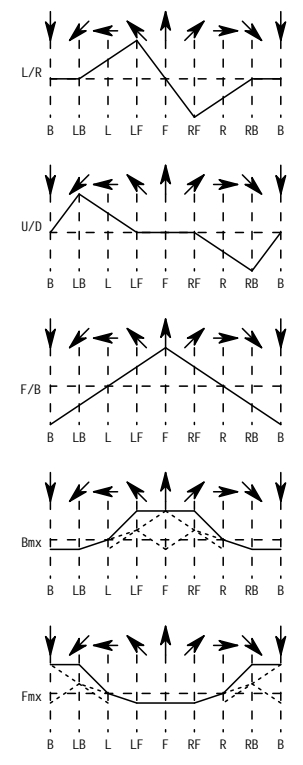
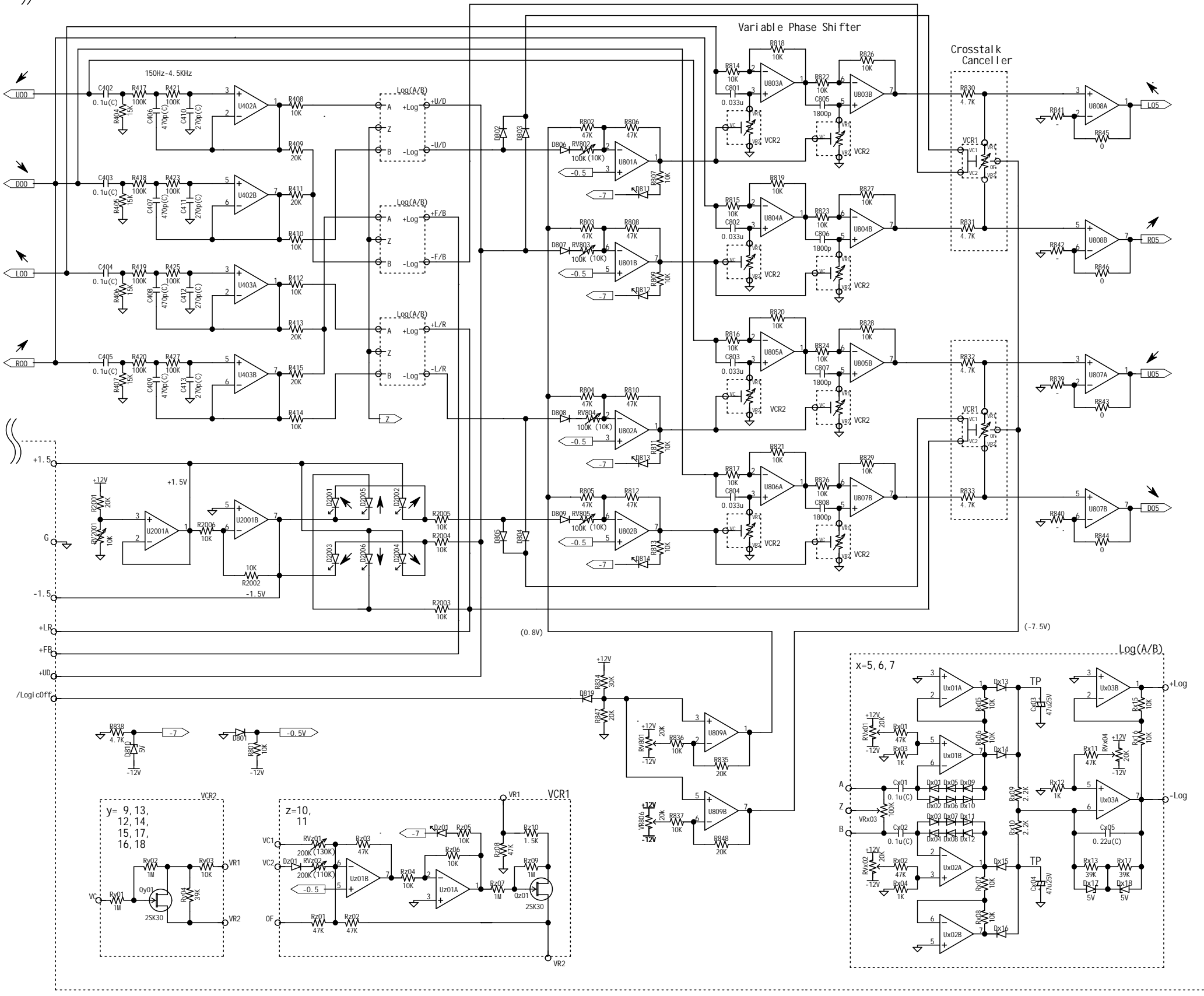
\*2 If exist high-frequency noise \*3 D6, D7 Solder on the back



Use this when you want to display source direction externally

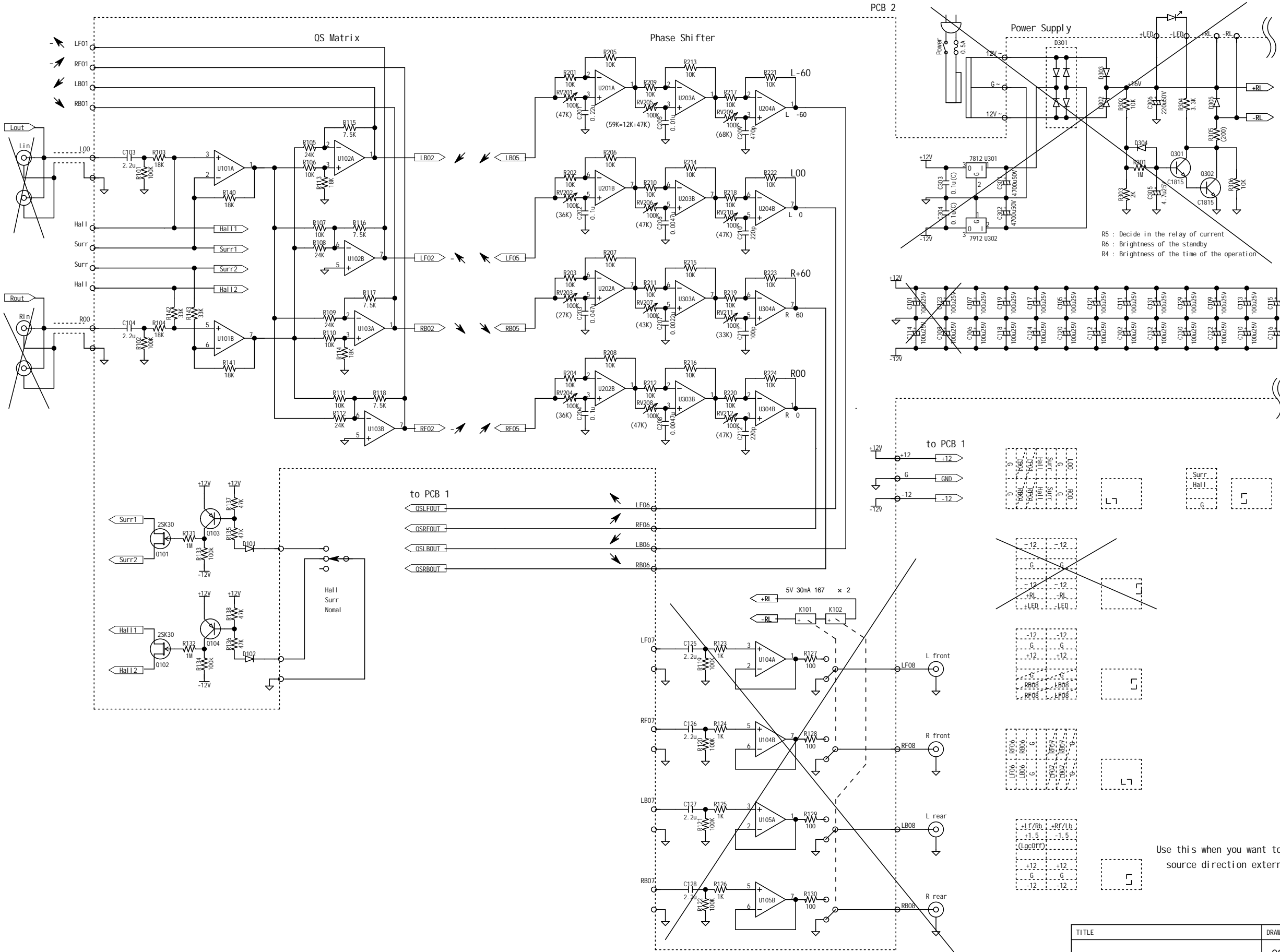
undeclared OPamp is 4558BD

TITLE		DRAWING No.	
		QS/SQ 4ch Logic Decoder 2 (page 1/4)	
SHEET	DATE	DESIGN	
1	2021/6/30		

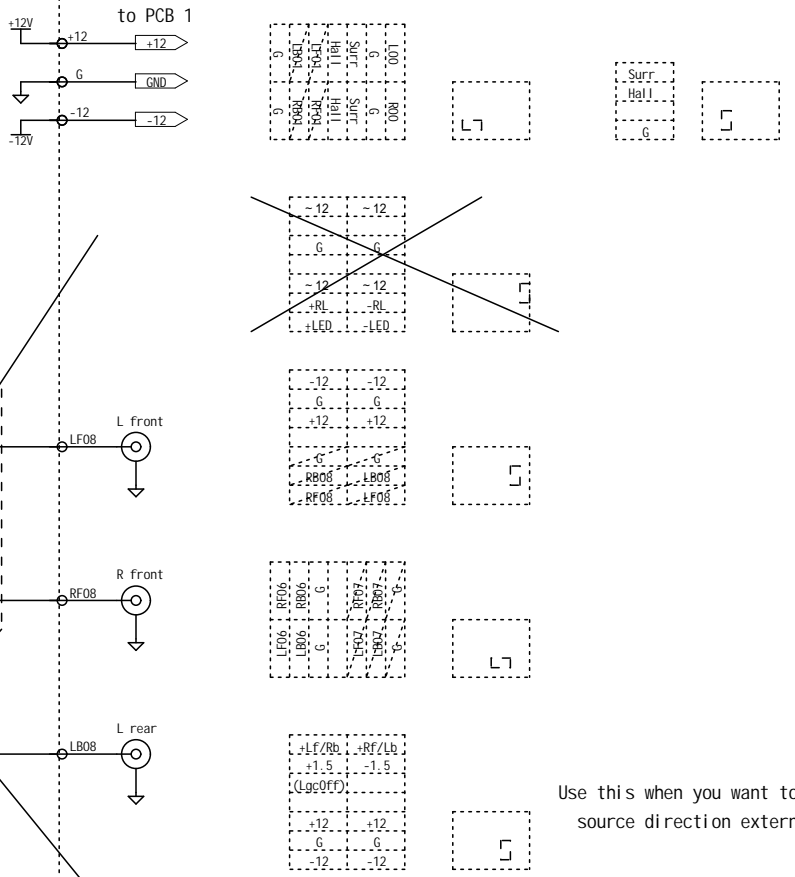


undeclared OPamp is 4558DD

TITLE		DRAWING No.	
		QS SQ 4ch Logic Decoder 2 (page 2/4)	
SHEET	DATE	DESIGN	
/	2021/6/30		



R5 : Decide in the relay of current  
 R6 : Brightness of the standby  
 R4 : Brightness of the time of the operation

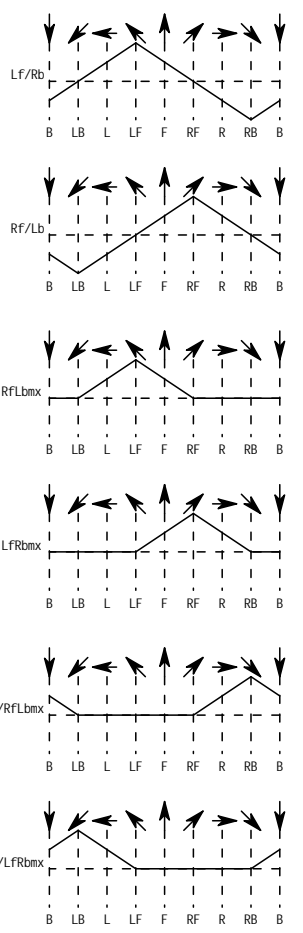
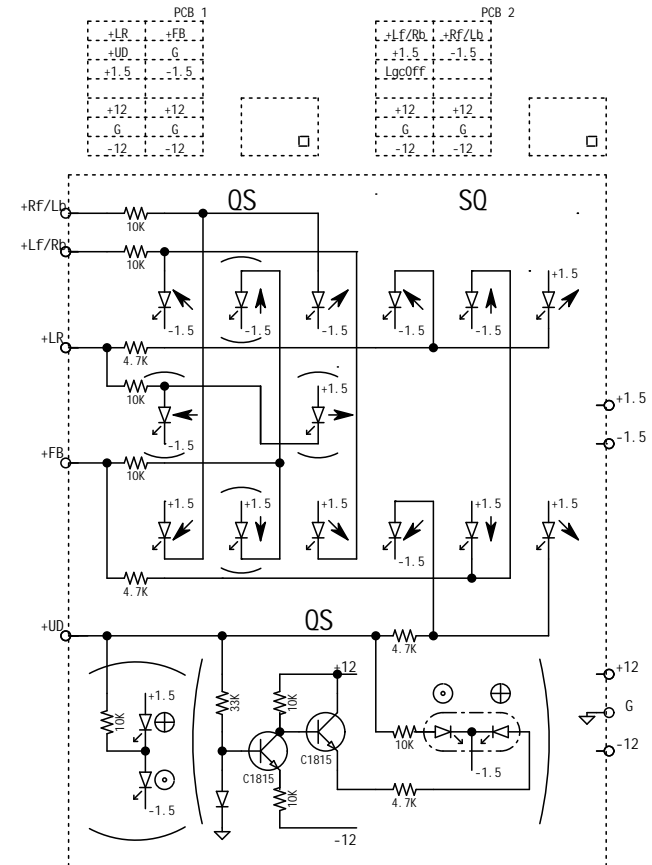


Use this when you want to display source direction externally

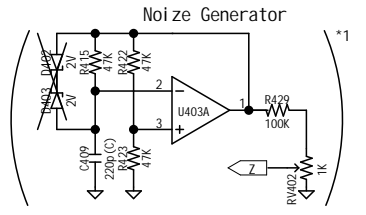
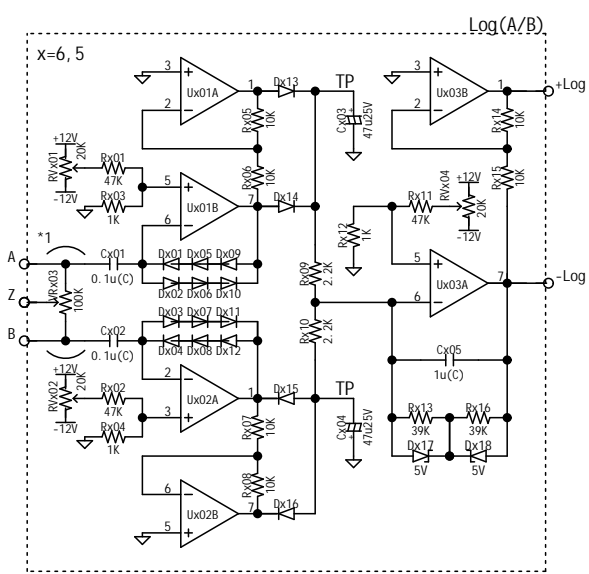
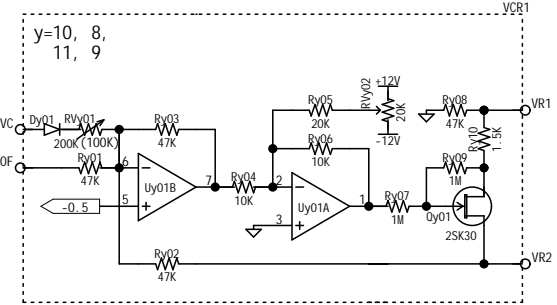
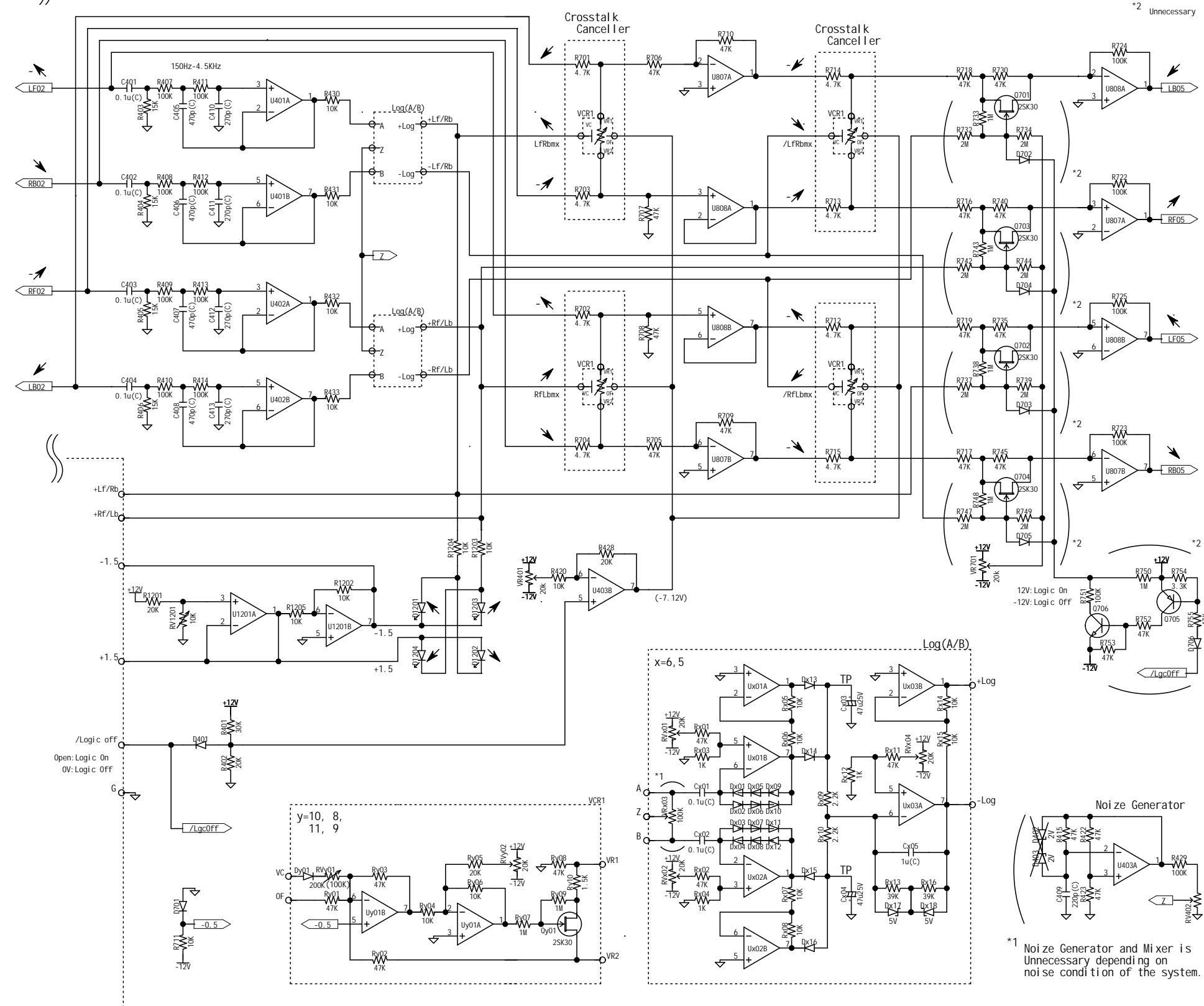
undeclared OPamp is 455800

TITLE		DRAWING No.	
		QS SQ 4ch Logic Decoder 2 (page 3/4)	
SHEET	DATE	DESIGN	
/	2021/6/30		

QS SQ direction detector of a sound source



PCB 2



\*1 Noise Generator and Mixer is Unnecessary depending on noise condition of the system.

undeclared OPamp is 45580D

TITLE		DRAWING No.	
		QS SQ 4ch Logic Decoder 2 (page 4/4)	
SHEET	DATE	DESIGN	
/	2021/6/30		