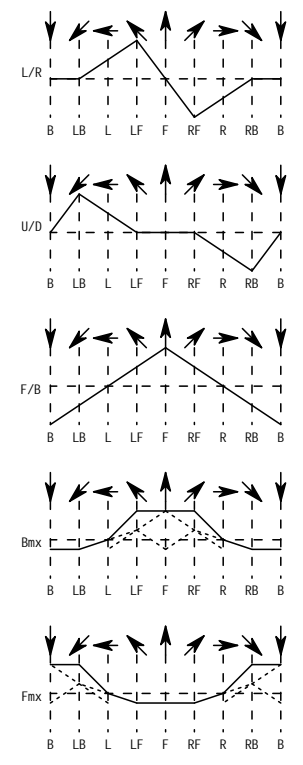
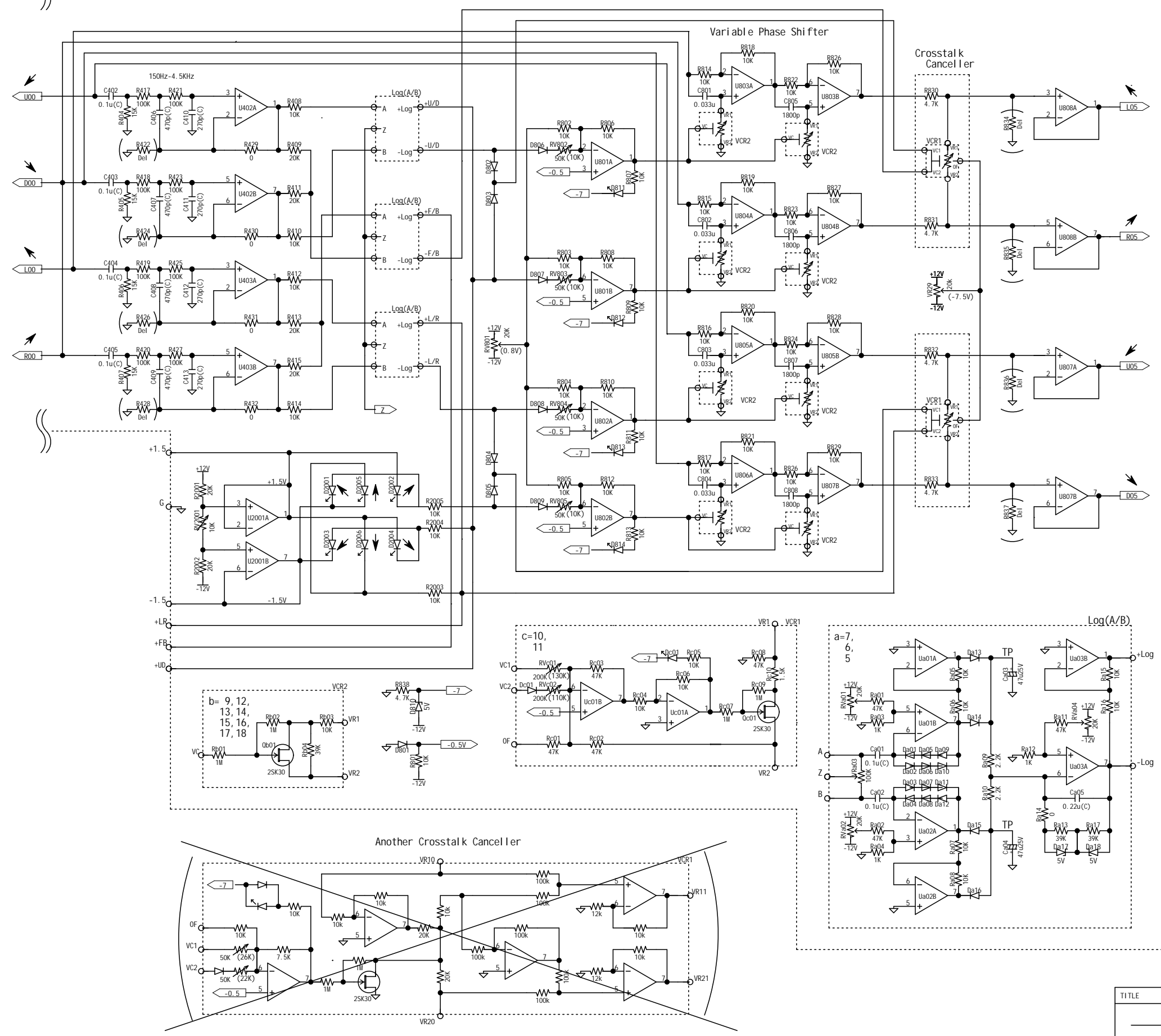


R5 : Decide from current of relay
R6 : Brightness of standby
R4 : Brightness during operation

*2 To reduce noise, attach to the back.

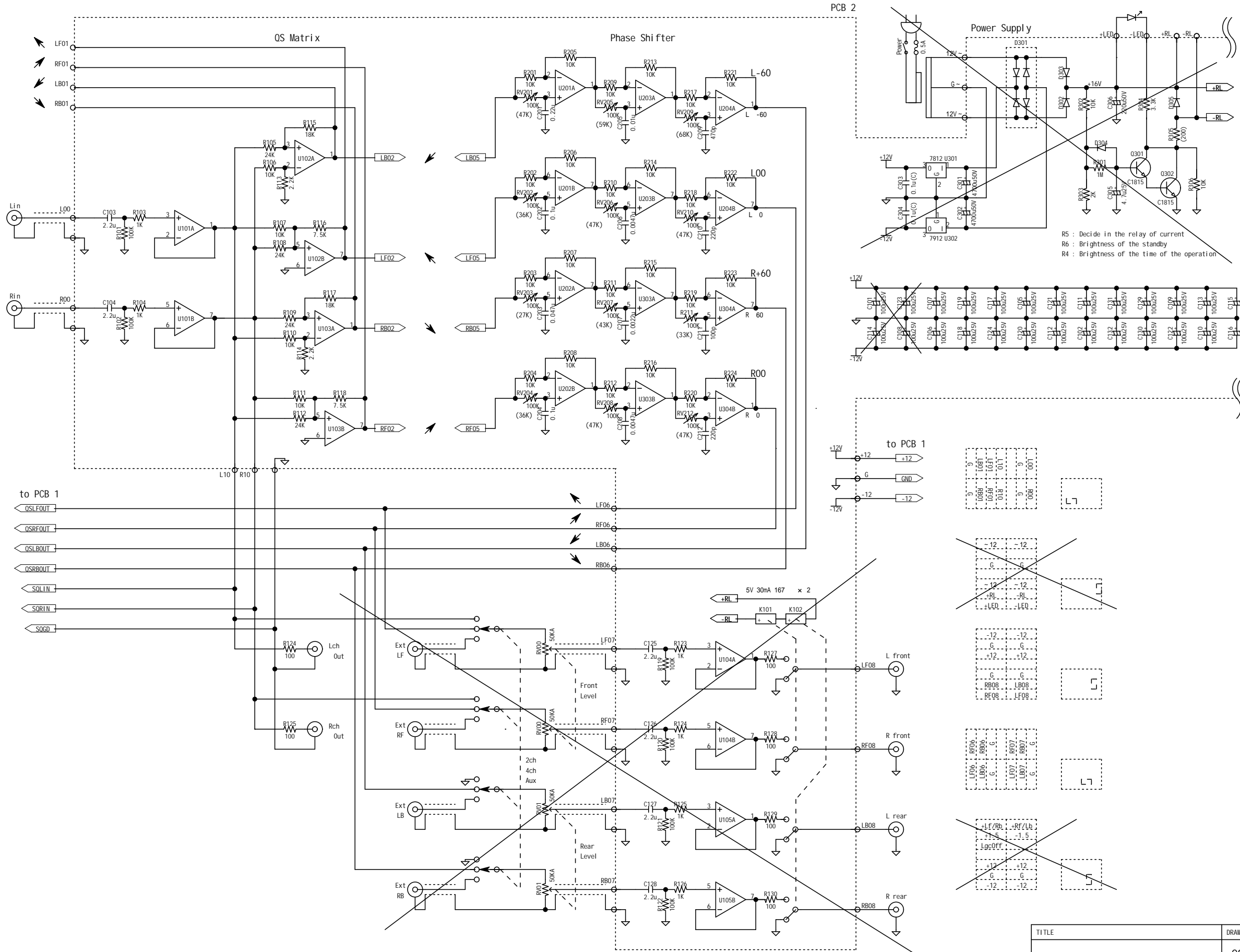
undecleared OPamp
is 45580D

TITLE		DRAWING No.	
		QS SQ 4ch Logic Decoder (page 1/4)	
SHEET	DATE	DESIGN	
/	2016/8/5		



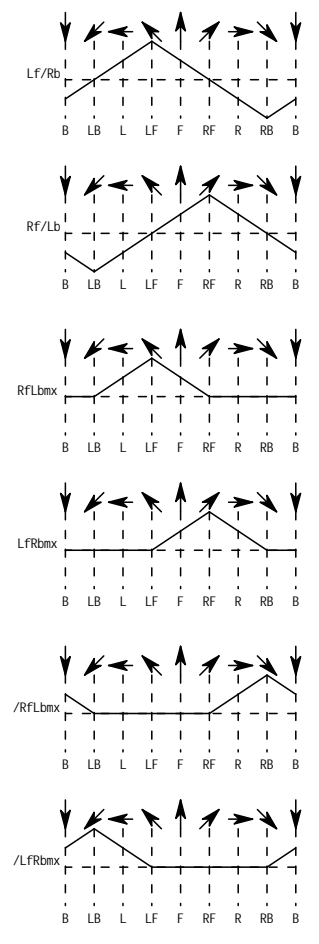
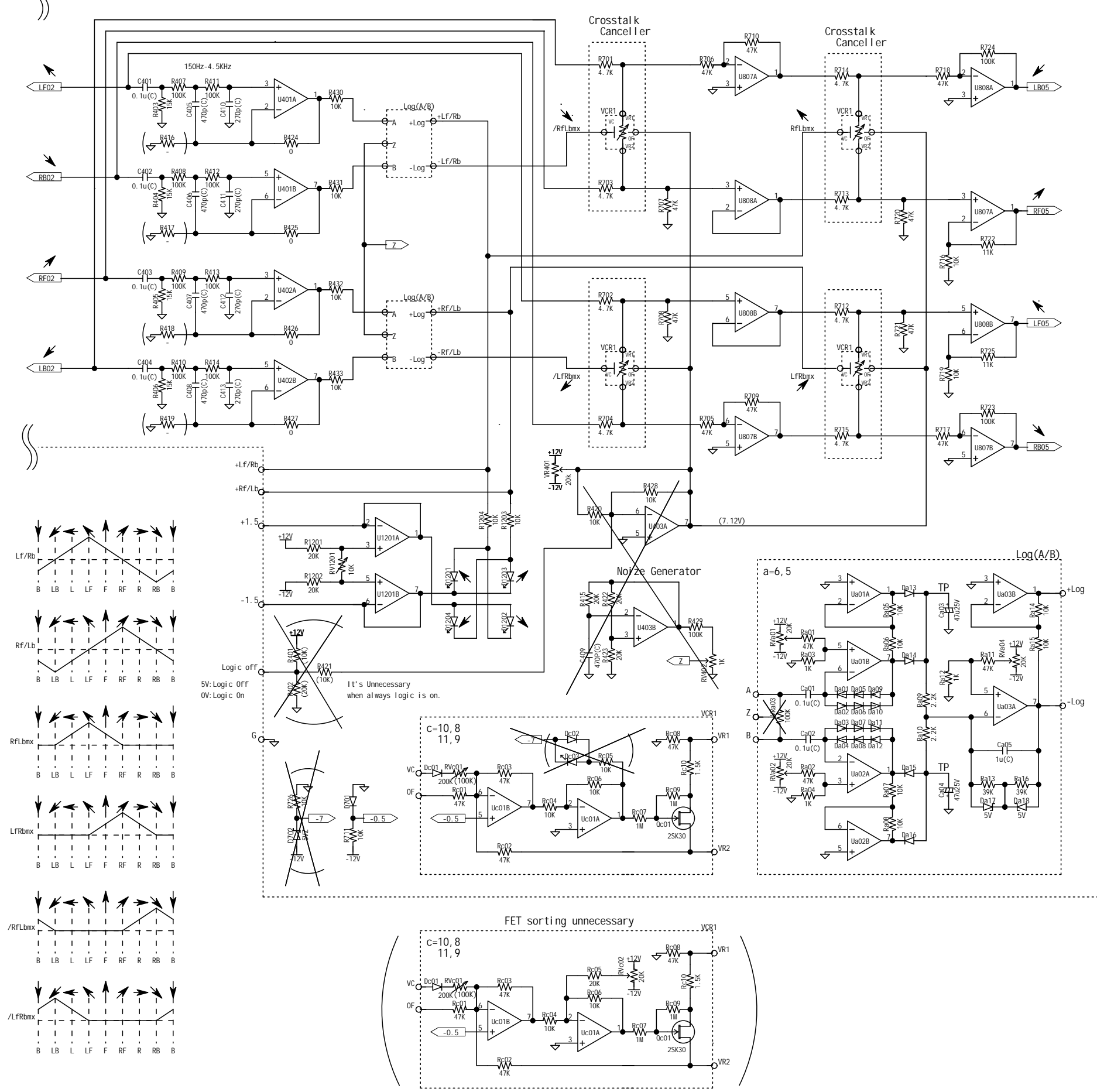
undeclared OPamp is 4558DD

TITLE		DRAWING No.	
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		(page 2/4)	
SHEET	DATE	DESIGN	
/	2016/8/5		



undeclared OPamp
is 455800

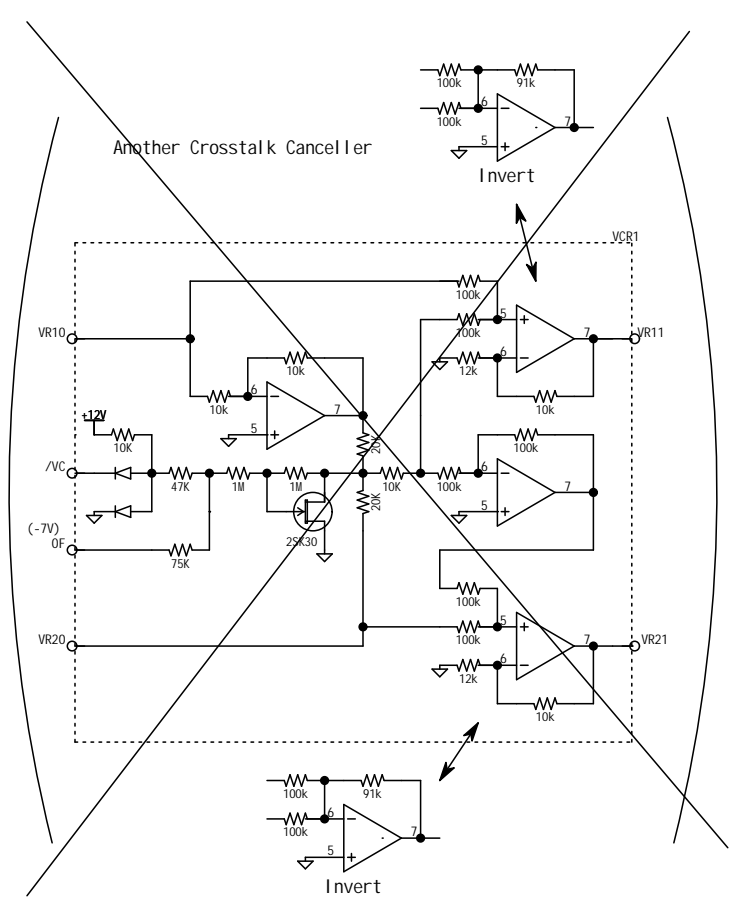
TITLE		DRAWING No.	
		QS SQ 4ch Logi c Decoder (page 3/4)	
SHEET	DATE	DESIGN	
1	2016/8/5		



5V: Logic Off
0V: Logic On

It's Unnecessary when always logic is on.

FET sorting unnecessary



TITLE		DRAWING No.	
		QS SQ 4ch Logic Decoder (page 4/4)	
SHEET	DATE	DESIGN	
	2016/8/5		

undeclared OPamp is 45580D